



Vidya Samvardhak Mandal's
VSM'S Somashekhar R. Kothiwale Institute of Technology, Nipani
DEPARTMENT of Electronics & Communication Engineering

TIME TABLE

Academic Year: 2022-23 [ODD-Sem]
Semester: VII

W.E.F: 19/09/2022
Lecture Hall No: 211

Day Time	9.30 to 10.25	10.25 to 11.20		11.35 to 12.30	12.30 to 01.25		02.00 to 05.00
MON	CRY	VLSI	B R E A K	RTS	CN	L U N C H	CN LAB (B1)
TUE	OE	CRY		CN	VLSI		VLSI LAB(B2)
WED	RTS	VLSI		OE	CRY		CN LAB (B2)
THU	CN	RTS		OE	Internship		VLSI LAB(B1)
FRI	PROJECT PHASE - I			PROJECT PHASE - I	Internship		T&P
SAT	PROJECT PHASE - I			PROJECT PHASE - I			

Subject	Subject Initial	Subject Code	Faculty Name	Faculty Initial
COMPUTER NETWORKS	CN	18EC71	PROF. ARATI B. SUDHAKAR	Prof. ABS
VLSI DESIGN	VLSI	18EC72	PROF. PRASHANT A. HEBBALE	Prof. PAH
REAL TIME SYSTEM	RTS	18EC731	DR. M. C. SARSAMBA	Dr. MCS
CRYPTOGRAPHY	CRYP	18EC744	PROF. SAPANA B. PATIL	Prof. SBP
OPEN ELECTIVE -B	OE	18XX75X	PROF. M. D. HANCHINAMATH	Prof. MDH
COMPUTER NETWORKS LAB	CN LAB	18ECL76	PROF. SAPANA B. PATIL	Prof. SBP
VLSI LABORATORY	VLSI LAB	18ECL77	PROF. PRASHANT A. HEBBALE/ PROF. SHWETA N.	Prof. PAH
PROJECT WORK PHASE - I	18ECP78	18ECP78	PROF. PRASHANT HEBBALE	Prof. PAH
TRAINING & PLACEMENT	-	-	DR. SUNIL I. SANGOLLI	Prof. SIS

Class Coordinator: Prof. Sapana B. Patil

Time table Coordinator

HOD

Dean Academic

Principal